



Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 3

Complete if Known

Application Number	10/815,604
Filing Date	03/31/2004
First Inventor	HO, Andrew
Art Unit	2611
Examiner	Perilla, Jason
Atty Docket #	
Rambus #	RA290.CIP1.US

US Patent Documents

Examiner Initials*	Cite No.	Document No.	Date (MM-DD-YYYY)	Patentee	Class	Subclass
	0000119 1	6055297	04/25/2000	Terry, John Brian	379	1
	0000192 2	5802073	09/01/1998	Platt, Alfred	371	22.5
	0000328 3	5732089	03/24/1998	Negi, Keiji	371	5.1
	0000375 4	2002-073373	06/13/2002	Nakao, Michinobu	714	738
	0000460 5	6230022	05/08/2001	Sakoda, Kazuyuki	455	522
	0000471 6	5228042	07/13/1993	Gauthier et al.	371	20.05
	0000592 7	5369755	11/29/1994	Berkovich, Semyon	375	500
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	0000653 9	5917856	06/29/1999	Torsti, Simo-Pekka	375	231
	0000663 10	6631486	10/07/2003	Komatsu, Yoshihide	714	724
	0000886 11	5761216	06/02/1998	Sotome, Tetsuo	371	27.1
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	0001081 14	6671847	12/30/2003	Chao et al.	714	744
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	0001362 16	6160790	12/12/2000	Bremer, Gordon	310	201
	0001406 17	6339387	01/15/2002	Koga, Makoto	341	100
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	0001876 19	6292116	09/18/2001	Wang, Bonnie I.	341	100
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	0001964 21	6606041	08/12/2003	Johnson, Brian	341	120
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**Examiner
Signature:**

Date Considered:

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Sheet 2 of 3	Complete if Known	
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US Patent Documents						
Examiner Initials*	Cite No.	Document No.	Date (MM-DD-YYYY)	Patentee	Class	Subclass
				Adam		
	0002852 29	6222380	04/24/2001	Gerowitz et al.	326	38
	0002996 30	5761212	06/02/1998	Foland, Jr.; William R.	371	21.2
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	0003556	EP 40	EP1134668A2	09/19/2001		G06F 13	40	
	0006306	WO 41	02093821	11/21/2002	Koninklijke Philips Elect	H04L	1/24	
	0006307	WO 42	00/27065	05/11/2000	Broadcom Corp	H04L	1/24	
	0006408	EP 43	1 127 423 A0	08/29/2001	Broadcom Corporation	H04L	1/24	

Non-Patent Literature Documents				T**
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
	0003614 44	GRANBERG, TOM, "Handbook of Digital Techniquis for High-Speed Design." Prentice Hall Modern Semiconductor Design Series. Copyright 2004 by Pearson Education, Inc. 12 pages.		
	0003620 45	MAXFIELD, C., "The Ouroboros of the Digital Consciousness: Linear-Feedback-Shift Registers," EDN, pp. 135-142, January 4, 1996		
	0003663 46	DALLY ET AL., "Multi-gigabit Signaling with CMOS," DARPA funded		

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	46 Cont.	presentation, 22 pages, May 12, 1997.	
	0003901 47	COVA ET AL., "Characterization of Individual Weights in Transversal Filters and Application to CCD's, IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 6, December 1982, pp. 1054-1061.	
	0003934 48	FARBER et al, "Wide-Band Network Characterization by Fourier Transformation of Time-Domain Measurements," IEEE Journal of Solid-State Circuits, Vol. SC-4, No. 4, August 1969, pp. 231-235	
	0003939 49	SOUMYANATH, K. et al, "Accurate On-Chip Interconnect Evaluation: A Time-Domain Technique." IEEE Journal of Solid-State Circuits, Vol. 34, No. 5, May 1999, pp. 623-631	
	0004095 50	SATO, T., et al., "Accurate In Situ Measurement of Peak Noise and Delay Change Induced by Interconnect Coupling," IEEE Journal of Solid-State Circuits, Vol. 36, No. 10, October 2001. pp. 1587-1591.	
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	0004714 53	HOROWITZ, MARK, "Lecture 15: Transmitter and Receiver Design." Computer Systems Laboratory - Stanford University. Copyright 2000. Pages 15-1 to 15-35.	
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